



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

#17

Applicant(s): Marc Tremblay

Title: GROUPING LOGIC CIRCUIT IN A PIPELINED SUPERSCALAR PROCESSOR

Application No.: 09/583,097

Filed: August 2, 1999

Examiner: David J. Huisman

Group Art Unit: 2183

Atty. Docket No.: 004-1391-1

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December 5, 2003

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PETITION FOR EXTENSION OF TIME

This is a petition under the provisions of 37 CFR 1.136(a) to extend the period for filing a response in the above-identified application. The requested extension and appropriate Fee are:

- ☒ One month extension (37 CFR 1.17(a)) \$110
☐ Applicant is a Small Entity. Therefore the amount shown above is reduced by one-half, and the resulting Fee is: n/a.

Payment is by:

- ☒ Check: An enclosed check includes the Fee.
☐ Deposit Account: Please charge the Fee to Deposit Account Number: n/a.
☒ The Commissioner is hereby authorized to charge any additional fees which may be required, or credit any overpayment, to Deposit Account Number: 50-0631.

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CERTIFICATE OF MAILING OR TRANSMISSION

I hereby certify that, on the date shown below, this correspondence is being

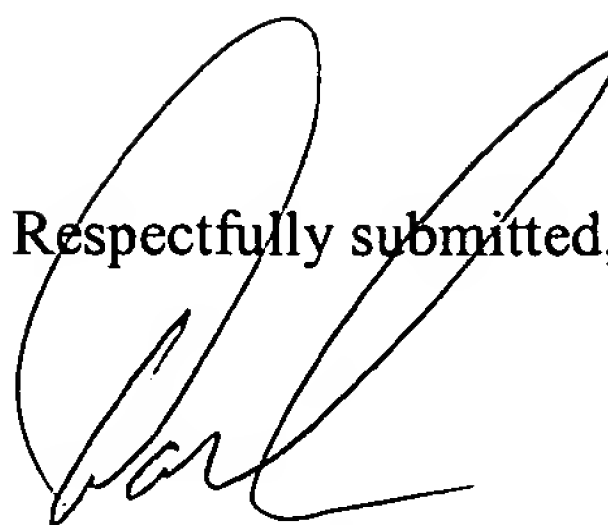
- ☐ deposited with the US Postal Service with sufficient postage as first class mail, in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.
- ☐ facsimile transmitted to the US Patent and Trademark Office.

David W. O'Brien

Date

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Respectfully submitted,



David W. O'Brien, Reg. No. 40,107
Attorney for Applicant(s)
(512) 338-6314
(512) 338-6301 (fax)